

Applicant: Dan M. White
Serial No.: 10/748,427
Filed : December 30, 2003
Page : 9 of 12

Attorney's Docket No.: Intel-017PUS
Intel Docket Number: P17944

REMARKS

Claims 1 to 3, 5, 7, 9 to 13, 15 to 18, 20 and 24 to 32 are pending in this application of which claims 1, 13 and 16 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Claims 1 to 3, 5, 7, 12, 13 and 15 were rejected under 35 U.S.C. § 103(a) as being obvious over Bade et al. (U.S. Patent Publication Number 20020059054 hereinafter "Bade") in view of Mulchandani et al. (U.S. Patent Number 5,701,488 hereinafter "Mulchandani"). Claim 9 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Mulchandani and Hoff et al. (U.S. Patent Number 5,778,231 hereinafter "Hoff"). Claims 10, 16 to 18 and 20 were rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Mulchandani and Hoff and in further view of Hall et al (U.S. Patent Number 4,720,778 hereinafter "Hall"). Claim 11 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Mulchandani and in further view of Smith et al. (U.S. Patent Number 6,311,324 hereinafter "Smith"). Claim 24 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Mulchandani, Hoff, Hall and Smith.

Amended claim 1 is directed to a method of displaying embedded firmware program information. The method includes displaying a first screen to interact with a user for high level function selections, displaying a second screen to show hardware resources for a programmable circuit, displaying a third screen to show source code for a plurality of source code programs to control the programmable circuit and displaying a fourth screen to render symbolic information

associated with the displayed source code. The symbolic information includes code labels, data labels referring to data structures including fields, data register names, and index register names; address locations for the code labels and the data labels; and listings associated with named registers, data labels for word, byte and short entities, and names of the data structures. The data structures and the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field.

The applied art is not understood to disclose or to suggest the foregoing features of claim 1. In particular, neither Bade nor Mulchandani discloses or suggests that the data structures and the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field (see page 7, lines 15 to 21 of Applicant's specification).

As indicated by the Examiner Bade does not disclose or suggest that the data structures are individually expandable (see page 4 of the Office Action). The Examiner has indicated that Mulchandani displays data structures that are individually expandable as shown in Table T-1 of Mulchandani (see Page 4 of the Office Action). Applicant respectfully submits that the data structures in Table T-1 are not individually expandable much less that fields of data structures also expandable. Therefore, Mulchandani does not disclose or suggest expandable symbolic information associated with data structures and displaying address and value information associated with data.

Accordingly, even if Mulchandani was combined with Bade, the hypothetical combination would not disclose or suggest that the data structures and the fields of the data

Applicant: Dan M. White
Serial No.: 10/748,427
Filed : December 30, 2003
Page : 11 of 12

Attorney's Docket No.: Intel-017PUS
Intel Docket Number: P17944

structures are individually expandable to show respective addresses and values of the word containing a start of the field. as recited in claim 1.

Claims 13 and 16 include the corresponding feature that the data structures and the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field as recited in claim 1. Applicant submits that the cited art should also be withdrawn with respect to claims 13 and 18 for at least the same reasons as claim 1.

Applicant submits that all dependent claims now depend on allowable independent claims.

For at least the foregoing reasons, Applicant requests withdrawal of the art rejections.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for withdrawing the prior art cited with regards to any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicant submits that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

Applicant's attorney can be reached by telephone at (781) 401-9988 ext. 123.

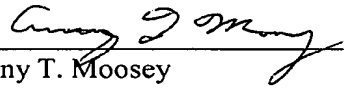
Applicant: Dan M. White
Serial No.: 10/748,427
Filed : December 30, 2003
Page : 12 of 12

Attorney's Docket No.: Intel-017PUS
Intel Docket Number: P17944

Enclosed is a Petition for a Three-Month Extension of Time. No other fee is believed to be due for this Response; however, if any other fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: Intel-017PUS.

Respectfully submitted,

Date: 12 March 2008



Anthony T. Moosey
Reg. No. 55,773

Attorneys for Intel Corporation
Daly, Crowley, Mofford & Durkee, LLP
354A Turnpike Street - Suite 301A
Canton, MA 02021-2714
Telephone: (781) 401-9988 ext. 123
Facsimile: (781) 401-9966